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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of: Larry Eugene Mosley
 Title: MULTI-LAYER CHIP CAPACITOR
 Attorney Docket No.: 884.240US1

1c511 U.S. PTO
 09/537274
 03/29/00

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Assistant Commissioner for Patents
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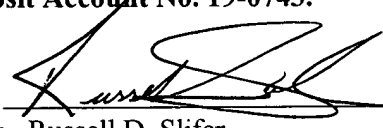
- X Utility Patent Application under 37 CFR § 1.53(b) comprising:
 X Specification (13 pgs, including claims numbered 1 through 21 and a 1 page Abstract).
 X Formal Drawing(s) (5 sheets).
 X Signed Combined Declaration and Power of Attorney (3 pgs).
 X Check in the amount of \$864.00 to pay the filing fee.
 X Assignment of the invention to Intel Corporation (2 pgs) and Recordation Form Cover Sheet.
 X Check in the amount of \$40.00 to pay the Assignment recording fee.
 X Information Disclosure Statement (1 pgs), Form 1449 (1 pgs), and copies of cited references (3).
 X Communication Concerning Co-pending Application(s) (1 pg).
 X Return postcard.

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	21 - 20 =	1	x 18 =	\$18.00
INDEPENDENT CLAIMS	5 - 3 =	2	x 78 =	\$156.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$690.00
TOTAL				\$864.00

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